

Homework #3: Stick diagrams

ECEn 451

Dr. David A. Penry

For all of the following problems, you should attempt to minimize poly twisting and maximize diffusion sharing.

1. Problem 1.18
2. For each of the following logic functions, provide a transistor-level schematic, a stick diagram, and an estimate of the area.
 - a) OR2
 - b) XOR2