

Homework #3: Stick diagrams and area estimation  
ECEn 451  
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For all of the following problems, you should attempt to minimize poly twisting and maximize diffusion sharing.

1. Problem 1.18
2. Problem 1.20
3. For each of the following logic functions, provide a transistor-level schemetic, a stick diagram, and an estimate of the area.
  - a) OR2
  - b) XOR2