

Homework #12: Sequential circuit element design
ECEn 451
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1. Problem 10.23
2. Problem 10.25
3. Design a latch which has the following problems (and only the problems listed):
 - a. A dynamic state node and exposed input diffusion
 - b. An exposed state node and a dynamic state node
4. When characterizing flops, why do we find the point at which the slope of the arrival time vs. t_{peq} curve is one?