Homework #1: Static CMOS combinational circuits

ECEn 451

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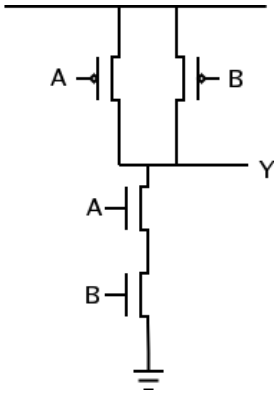
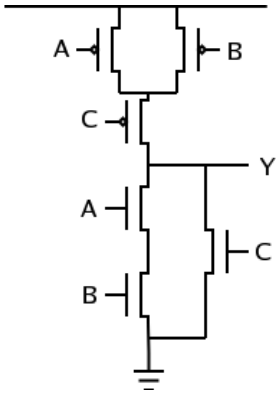
1. Identify the logic function of each of the following circuits:

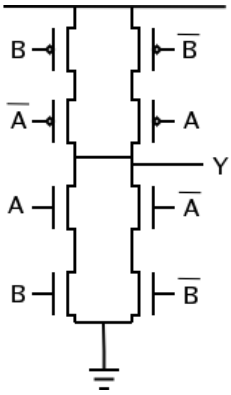
a)



b)



c)



2. Draw a transistor-level static CMOS schematic using the minimum number of transistors for each of the following gates (remember to label all inputs, outputs, and supply rails):
   a) NOR3
   b) NAND4